|  |  |
| --- | --- |
| 1. | Segment register use to hold\_\_\_\_\_\_\_\_\_\_\_\_\_\_.  **FILL IN THE BLANKS OF MICROPROCESSOR & MICROCONTROLLER** |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | base Address. | |  | | |  | |  | | |
| 2. | The Address unit generates physical address by using\_\_\_\_\_\_\_\_\_and \_\_\_\_\_\_\_\_\_\_\_ . |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | base Address | |  | | |  | |  | | |
| 3. | Microprocessor 80386 is\_\_\_\_\_bit processor |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 32 bit | |  | | |  | |  | | |
| 4. | Maximum size of 80386 segment is\_\_\_\_. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 4GB | |  | | |  | |  | | |
| 5. | A page memory of 80386 system is\_\_\_\_\_. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 4KB | |  | | |  | |  | | |
| 6. | Pre-fetched instruction queue of 80386 is\_\_\_\_\_\_. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 16Byte | |  | | |  | |  | | |
| 7. | Microprocessor 80386 can address\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_byte physical |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 4 gega | |  | | |  | |  | | |
| 8. | Dual core processor is\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_bit processor. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 32 bit | |  | | |  | |  | | |
| 9. | Core 2 Dual processor is\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_bit processor. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 64bit | |  | | |  | |  | | |
| 10. | Examples of 2-byte instruction -------------------------of 8086MP. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | mov al, [bx] | |  | | |  | |  | | |
| 11. | Examples of 4-byte instruction -------------------------of 8086MP. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | mov al, [4527H] | |  | | |  | |  | | |
| 12. | PUSHF means -------------- |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | store flage register to stack | |  | | |  | |  | | |
| 13. | POPF means ------------- |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | load flage register from stack | |  | | |  | |  | | |
| 14. | The function of ALE is ---------------- |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | demux Address from Address and data | |  | | |  | |  | | |
| 15. | RAM of 1K\* 8 means -------------------- |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 1Kilo Byte | |  | | |  | |  | | |
| 16. | Examples of direct addressing mode is -----------------. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | mov [2756], al | |  | | |  | |  | | |
| 17. | Examples of indirect addressing mode is -----------------. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | mov [bx], al | |  | | |  | |  | | |
| 18. | Examples of register addressing mode is -----------------. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | mov al, bl | |  | | |  | |  | | |
| 19. | Examples of implicit addressing mode is -----------------. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | mov al, 45H | |  | | |  | |  | | |
| 20. | Examples of control signals of 8086 ----------------------. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | RD or WR | |  | | |  | |  | | |
| 21. | The microprocessor comprises of ------------------. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | EU and BIU | |  | | |  | |  | | |
| 22. | Memory Addressing capacity of 8086 microprocessor is of ------------- . |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 1MB | |  | | |  | |  | | |
| 23. | The address bus width of 64 KB of memory is ----------------. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 16 bit | |  | | |  | |  | | |
| 24. | The instruction MOV AL,BL belongs to ----------------- addressing mode |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | register addressing | |  | | |  | |  | | |
| 25. | \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_Pre-Fetch instruction queue use in 8086MP |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 6 Byte | |  | | |  | |  | | |
| 26. | BOR stand for\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | brown out reset | |  | | |  | |  | | |
| 27. | WDT stand for\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | Watch Gog Timer | |  | | |  | |  | | |
| 28. | POR stand for\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | Power On Reset | |  | | |  | |  | | |
| 29. | RISC stand for\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | Reduced Instruction Set Computer | |  | | |  | |  | | |
| 30. | I2C stand for\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | Inter IC(Integrated-circuit) communication | |  | | |  | |  | | |
| 31. | SPI stand for\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | Serial Port Interface | |  | | |  | |  | | |
| 32. | Address bus of file register MC PIC16F877A is\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_bit. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 9 | |  | | |  | |  | | |
| 33. | BTFSS stand for\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | Bit Test Skip next Instruction if Set | |  | | |  | |  | | |
| 34. | BTFSC stand for\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | Bit Test Skip next Instruction if Clear | |  | | |  | |  | | |
| 35. | DECFSZ stand for\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | decreament file registor skip next instruction if Zero flage is set | |  | | |  | |  | | |
| 36. | TRISB is\_\_\_\_\_\_\_\_\_\_\_\_\_\_ofportB. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | control registor | |  | | |  | |  | | |
| 37. | PORTC register is\_\_\_\_\_\_\_\_\_\_\_\_\_\_of port C. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | Data registor | |  | | |  | |  | | |
| 38. | Basic Micro Processor is consist of \_\_\_\_\_\_\_\_\_,\_\_\_\_\_\_\_\_\_\_\_,\_\_\_\_\_\_\_\_and \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | CU,ALU,Register and internal data bus | |  | | |  | |  | | |
| 39. | ALU stand for\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) |  | |  | | |  | |  | | |
| 40. | CU consist of\_\_\_\_\_\_\_\_\_\_\_\_\_,\_\_\_\_\_\_\_\_\_\_\_and\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | pulsh gen, sequence counter and control cct | |  | | |  | |  | | |
| 41. | Accumulator Holds \_\_\_\_\_\_\_\_\_\_of\_\_\_\_\_\_\_\_\_\_\_ . |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | result of ALU | |  | | |  | |  | | |
| 42. | Out put of any register is \_\_\_\_\_\_\_\_\_\_by\_\_\_\_\_\_\_. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | isolated by tristat | |  | | |  | |  | | |
| 43. | Register is consist of \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) |  | |  | | |  | |  | | |
| 44. | A Instruction cycle is consist of\_\_\_\_\_\_\_\_\_,\_\_\_\_\_\_\_\_,\_\_\_\_\_\_\_\_\_ and \_\_\_\_\_\_\_\_\_\_Machine cycle |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | fetch, decode, execuite and store back | |  | | |  | |  | | |
| 45. | Tri state is use for isolate \_\_\_\_\_\_\_\_\_\_\_\_and \_\_\_\_\_\_\_\_\_\_. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | out put of register and BUS | |  | | |  | |  | | |
| 46. | All controls in computer is gen by \_\_\_\_\_\_\_\_\_\_\_\_of \_\_\_\_\_\_\_\_\_\_\_\_ |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | control and timing gen unit of Micro Processor | |  | | |  | |  | | |
| 47. | Memory is consist of \_\_\_\_\_\_\_\_\_\_\_\_\_with\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | gruop of regisret with decoding mechanism | |  | | |  | |  | | |
| 48. | Segment Register of Micro Processor 8086 is\_\_\_\_\_\_\_\_\_bit. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 16 | |  | | |  | |  | | |
| 49. | Data bus of 8086 microprocessor is of -------- bits |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 16 | |  | | |  | |  | | |
| 50. | Address bus of 8086 microprocessor is of -------- bits. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 20 | |  | | |  | |  | | |
| 51. | Total No of segment register in 8086 MP is\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 4 | |  | | |  | |  | | |
| 52. | Total 16 bit general propose register in MP8086 is\_\_\_\_\_\_\_\_\_ |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | 8 | |  | | |  | |  | | |
| 53. | IP register use to hold \_\_\_\_\_\_\_\_\_\_of\_\_\_\_\_\_\_\_\_. |
|  | |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | | |  |  | | --- | --- | | (a) | Off set address of next instruction | |  | | |  | |  | | |
|  |  |
|  |  |
|  |  |
|  |  |